
Smart Pc Care Registration Key 21rar !FREE!



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Someone please give me the serial number to install in the software. The serial number in this software is different from the serial numbers in the linked downloaded. I tried multiple links but that didnt work1. Field of the Invention This invention relates to a method of operating a memory system having a memory module including a plurality of memory devices and, more particularly, to a method of operating a memory module in a dynamic random access memory (DRAM) device. 2. Description of Related Art Please refer to FIG. 1. FIG. 1 is a timing diagram illustrating the positive and negative voltage pulses applied to the gate of a MOS transistor in a memory cell 11 of a dynamic random access memory (DRAM) according to the prior art. As shown in FIG. 1, the on/off state of the MOS transistor in the memory cell 11 is controlled by the voltage pulses S1 and S2. The sense amplifier 12 compares the voltage of a bit line BL, coupled to the memory cell 11, with that of a reference voltage Vref (i.e. the voltage provided by a reference cell 14). If the voltage on the bit line BL is smaller than the reference voltage Vref, the MOS transistor in the memory cell 11 is in the on state; if the voltage on the bit line BL is larger than the reference voltage Vref, the MOS transistor in the memory cell 11 is in the off state. In the DRAM 11, charge (here, positive charges) accumulates on the node N1 when the voltage on the node N1 is smaller than the reference voltage Vref. When the voltage on the node N1 is larger than the reference voltage Vref, the charge on the node N1 is swept away to the substrate 12. It is thus clear that the prior art DRAM 11 is a capacitor-based device. Please refer to FIG. 2. FIG. 2 is a block diagram of a DRAM employing a capacitor array. In the FIG. 2 DRAM, a transistor 15 is coupled to a memory cell 11 having a storage capacitor 16. The gate of the transistor 15 is coupled to a word line 17. The storage capacitor 16 is coupled between the drain of the transistor 15 and a bit line BL. The source of the transistor 15 is coupled to ground. A positive voltage pulse S1 is applied to the word 6d1f23a050

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